

ABSTRACT

An auto power saving device for multi-channel transceiver comprises at least one tier and five levels, in which the five levels comprises an input level for each tier having at least one input and producing a time delayed binary signal, a NAND gate level having one
5 NAND gate for each tier, each NAND gate receiving a signal from its respective input and from the output of an upper neighboring NAND gate if such NAND gate exists and from the output of a lower neighboring NAND gate if such NAND gate exists, an inverter level comprising one inverter per tier receiving and inverting said signal from its
10 respective NAND gate, and a NOR gate level comprising one NOR gate that receives all inputs from all inverters on all tiers, and an Output Level producing an output signal.